

# Measure and analysis of 4H-SiC Schottky barrier height with Mo contacts

Teng Zhang<sup>\*</sup>, Christophe Raynaud, and Dominique Planson

UDL, INSA Lyon, Université Claude Bernard Lyon 1, Ecole Centrale de Lyon, CNRS, Ampère, 69621 Villeurbanne, France

Received: 28 September 2018 / Received in final form: 14 December 2018 / Accepted: 20 December 2018

**Abstract.** Current–voltage ( $I$ – $V$ ) and capacitance–voltage ( $C$ – $V$ ) characteristics of Schottky Mo/4H-SiC diodes have been measured and analyzed as a function of temperature between 80 and 400 K. The  $I$ – $V$  characteristics significantly deviate from ideal characteristics predicted by the thermionic emission model because of the inhomogeneity of Schottky contact. After a brief review of the different existing models, the main parameters (ideality factor, barrier height, and effective Richardson constant) of both diodes have been extracted in the frame of a Gaussian barrier height distribution model, whose mean and standard deviation are linearly dependent on voltage and temperature, as well as in the context of the potential fluctuation model. The results are compared with the values extracted by  $C$ – $V$  and the values in the literature. A link is established between these two models. Diodes of different  $I$ – $V$  characteristics, either identified as single barrier or double barrier, have been analyzed by Deep Level Transient Spectroscopy (DLTS) to investigate the deep level defects present. No noticeable difference has been found.

## 1 Introduction

Since many years, it has been observed that the current–voltage ( $I$ – $V$ ) characteristics of Schottky diodes on SiC are rarely correctly modeled by a simple model of thermionic injection of electrons over a barrier height [1,2]. This phenomenon is also observed in Schottky on inorganic semiconductor [3]. The thermionic-emission (TE) model gives the forward current  $I$  through the diode by the formula:

$$I = AA^*T^2 e^{-\frac{q\Phi_B}{kT}} \left[ e^{\frac{q(V-IR_s)}{kT}} - 1 \right] \quad (1)$$

where  $V$  is the voltage across the Schottky diode,  $R_s$  is its series resistance (including resistance of epitaxial, substrate, and back face Ohmic contact),  $A$  the surface of Schottky contact,  $A^*$  the effective Richardson constant,  $T$  the temperature,  $\Phi_B$  the barrier height of the Schottky contact that is assumed constant,  $q$  the elementary charge, and  $k$  the Boltzmann constant. The term preceding the brackets is called saturation current  $I_s$ . In the region where  $R_s$  is negligible and  $V > 3kT/q$ , the relationship can be simplified as:

$$I = I_s e^{\frac{qV}{kT}}. \quad (2)$$

Therefore, the plot of  $\ln(I)$  versus  $V$  should give a straight line whose slope is  $q/kT$  and the intercept with  $y$ -axis makes it possible to calculate  $I_s$ .

In reality, the slope is rarely equal to  $q/kT$  and therefore more complex models, ranging from double barrier height models to the Gaussian barrier height distribution as well as models in which  $\Phi_B$  depends on  $V$  and/or  $T$  have been published [4–6]. Particularly the Gaussian model explains rather well the measurements made on the Schottky diodes with deposit of Mo on 4H-SiC.

We present an analysis of these different models and apply them to the study of molybdenum Schottky contacts on n-type 4H-SiC epitaxies. The  $I$ – $V$  and  $C$ – $V$  characteristics have been measured between 80 and 400 K and the results have been analyzed to extract in particular the barrier heights and ideality factors.

## 2 Analytical models

### 2.1 The ideality factor and the barrier height at 0 V

Considering that the Schottky barrier height (SBH) varies linearly with the voltage as  $\Phi_B = \Phi_{B0} + \gamma V$ , where  $\Phi_{B0}$  is the barrier height at 0 V and  $\gamma (= \partial\Phi_B/\partial V)$  is positive, equation (1) becomes:

$$I = I_s e^{\frac{q(V-IR_s)}{nkT}} \left[ 1 - e^{-\frac{q(V-IR_s)}{kT}} \right] \quad (3)$$

with the saturation current  $I_s$  given by:

$$I_s = AA^*T^2 e^{-\frac{q\Phi_{B0}}{kT}} \quad (4)$$

where  $n = 1/(1 - \gamma)$  is recognized as the ideality factor of the diode [7].

<sup>\*</sup> e-mail: [teng.zhang@insa-lyon.fr](mailto:teng.zhang@insa-lyon.fr)

According to equations (3) and (4), the relationship between  $\log(I)$  and  $V$  should be linear if the series resistance is small enough to be neglected. Therefore, the ideality factor and the barrier height at 0 V can be determined from the  $I$ - $V$  plot.

Hackam and Harrop [8] have proposed that, to take into account, the effect of a thin interfacial layer as well as the effect of image force and the presence of surface charges on the barrier height, which also manifest at 0 V and result in  $n \neq 1$ , it is necessary to integrate the ideality factor into the saturation current as well. Therefore, equation (4) is amended as follows:

$$I_s = AA^*T^2 e^{-\frac{q\Phi_{B0}}{nkT}}. \quad (5)$$

## 2.2 $T_0$ effect

In order to precisely fit the experimental  $I$ - $V$  characteristics of Schottky diodes, where a variation of  $n$  with temperature is observed, Padovani and Sumner [9] have suggested to replace  $T$  by  $T + T_0$  in the TE model, therefore equation (1) becomes (neglecting  $R_s$ ):

$$I = AA^*T^2 e^{-\frac{q\Phi_{B0}}{k(T+T_0)}} \left[ e^{\frac{qV}{k(T+T_0)}} - 1 \right] \quad (6)$$

where  $T_0$  neither depends on the temperature nor the voltage over a wide temperature range. Thus, this relation is equal to introduce an ideality factor in the two exponential terms:

$$n = 1 + \frac{T_0}{T}. \quad (7)$$

## 2.3 Gaussian distribution of barrier heights

To explain the difference between SBH measured by  $I$ - $V$  and  $C$ - $V$  over the range of measured temperatures, Song et al. [4] have assumed that the barrier height is normally distributed with a probability density:

$$P(\Phi_B) = \frac{1}{\sigma_s \sqrt{2\pi}} e^{-\frac{\bar{\Phi}_B - \Phi_B}{2\sigma_s^2}} \quad (8)$$

where  $\bar{\Phi}_B$  is the mean barrier height and  $\sigma_s$  is its standard deviation.

As a general SBH distribution, equation (8) can also be applied at 0 V (the zero bias condition). In that case, the SBH can be expressed as:

$$\Phi_{B0} = \bar{\Phi}_{B0} - \frac{\sigma_{s0}^2}{2kT/q} \quad (9)$$

where  $\bar{\Phi}_{B0}$  and  $\sigma_{s0}$  are the corresponding parameters in Gaussian distribution at 0 V. Practically, the SBH at 0 V  $\Phi_{B0}$  can be calculated at each temperature according to equation (4) if we know the value of  $A^*$ . Thus, it is possible to extract  $\bar{\Phi}_{B0}$  and  $\sigma_{s0}$  by tracing  $\Phi_{B0}$  versus  $q/2kT$ .

## 2.4 Potential fluctuation model

This model has been developed by Werner and Guttler [5] to explain the temperature dependence of the ideality factor. The barrier height is assumed to be normally distributed, but the mean and standard deviation are assumed to vary linearly with voltage as:

$$\bar{\Phi}_B = \bar{\Phi}_{B0} + \rho_2 V \quad \text{and} \quad \sigma_s^2 = \sigma_{s0}^2 + \rho_3 V.$$

We can then show that the ideality factor can be expressed in the form:

$$\frac{1}{n} - 1 = -\rho_2 + \frac{q\rho_3}{2kT}. \quad (10)$$

By tracing  $n^{-1} - 1$  versus  $q/2kT$ , the values of  $\rho_2$  and  $\rho_3$  are obtained respectively from the slope and the y-intercept.

## 2.5 Flat-band barrier height

Since the barrier height depends on the bias voltage, i.e. in fact the electric field, Wagner et al. [6] have proposed to define the barrier height under flat-band condition (zero field)  $\Phi_{BF}$ , which would be a more fundamental magnitude due to independence of the applied voltage and is given by:

$$\Phi_{BF} = n\Phi_{B0} - (n-1) \frac{kT}{q} \ln \frac{N_C}{N_D}. \quad (11)$$

## 2.6 Richardson plot and its modifications

To determine the effective Richardson constant  $A^*$ , the Richardson plot, based on equations (1) and (2),  $\ln(I_s/T^2)$  versus  $1/T$  should be linear with a slope related to the barrier height and an intercept bound to  $A^*$ .

According to equations (5), (9), and (11), the barrier height is determined from so-called modified Richardson (MR) plots, of which there are several variants. The plot of  $\ln(I_s/T^2)$  versus  $1/nT$  (Eq. (5), [8]),  $\ln(I_s/T^2) - q^2\sigma_{s0}^2/2K^2T^2$  versus  $q/kT$  (Gaussian model, [10]) or  $\ln(I_s/T^2) + (1-1/n) \ln(N_C/N_D)$  versus  $1/nT$  ([11]) allow to determine the barrier height at 0 V or in flat-band condition.

## 2.7 $C$ - $V$ characteristics

In the case of an n-type Schottky diode with an uniform doping  $N_D - N_A$ , the capacitance  $C$  is related to the width of the space charge region  $W$ :

$$C = \frac{\varepsilon_{sc} A}{W} \quad (12)$$

and  $W$  relates to the doping concentration by

$$W = \sqrt{\frac{2\varepsilon_{sc}}{q(N_D - N_A)} (V_I - V_r)} \quad (13)$$

where  $\varepsilon_{sc}$  is the dielectric permittivity of the semiconductor and  $V_r$  the reverse polarization.

If doping is uniform, the tracing of  $1/C^2 = f(V_r)$  must be straight, and the intersection  $V_I$  with x-axis gives the barrier height  $\Phi_B^{C-V}$  [12]:

$$\Phi_B^{C-V} = V_I + \frac{kT}{q} + (E_c - E_f). \quad (14)$$

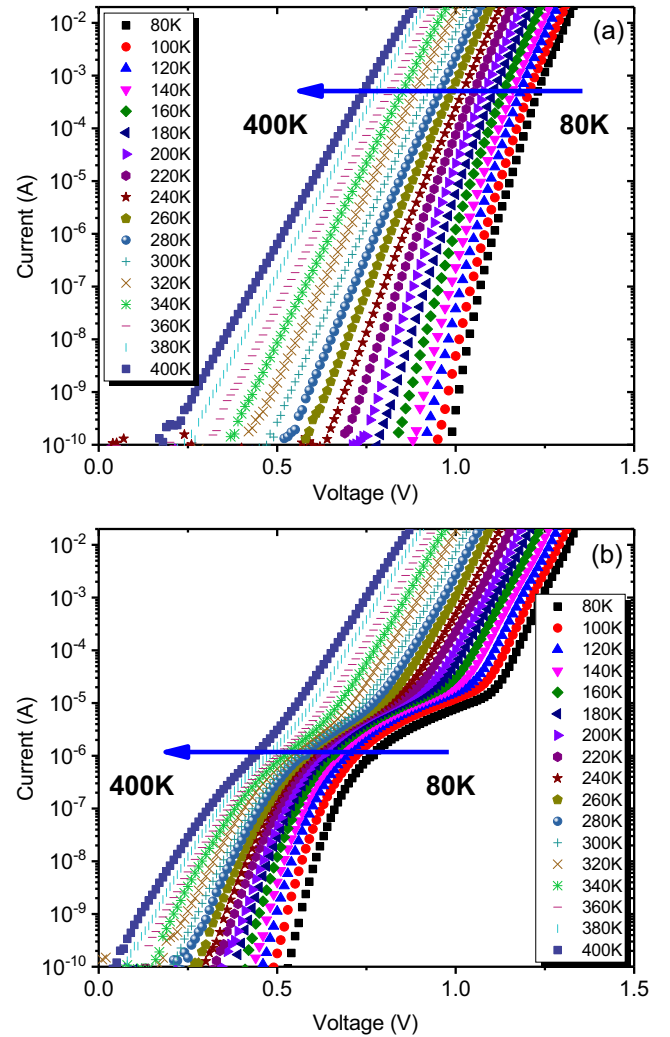
### 3 Experimental setup

Two n-type Mo/4H-SiC Schottky diodes on the same wafer that were provided by our collaborator have been characterized and noted (a) and (b). The molybdenum anode is in square shape with a surface area of  $2.48 \text{ mm}^2$ . A post-annealing of  $500^\circ\text{C}$  were taken out after the lift-off process during the fabrication. The direct  $I-V$  measurements have been made by the 4-cable method (Kelvin method) with Keithley K2410 between 80 and 400 K with a step of 20 K in a cryostat and under darkness. The  $C-V$  measurements, between 0 and  $-5 \text{ V}$ , were performed under the same conditions at 100 kHz with a Keysight E4990A impedance analyzer. The analysis of the electrically active defects has then been conducted with a PhysTech FT-1230 High Energy Resolution Analysis Deep Level Transient Spectroscopy (HERA DLTS) bench between 20 and 550 K. The reverse-state voltage is  $V_R = -10 \text{ V}$  and the filling pulse voltage is  $V_P = -0.1 \text{ V}$  for a duration of  $t_P = 100 \mu\text{s}$  for all DLTS tests. Depending on the temperature range, the DLTS technique used is either DLTS in current mode (measure of current transients) at low temperature (20–170 K), or DLTS in capacitance mode at higher temperature (200–550 K) for the propose of optimal precision. In order to avoid an annealing effect under the temperature ramp, a fast preliminary DLTS scan has been carried out on both diodes (from 300 to 550, then back to 300 K for a total of 3 hours).

## 4 Results and discussion

### 4.1 Forward $I-V$ characteristics

In Figure 1, the  $\log(I)-V$  characteristics of the diode (a) are almost linear over the entire temperature range. On the contrary, for the diode (b), the  $I-V$  curves clearly show a phenomenon of multi-barrier, especially at low temperature. Indeed, under high current ( $>10 \mu\text{A}$ ) even at 80 K,  $\log(I)-V$  is linear as for diode (b). While under low voltage, the current is abnormally strong compared to the diode (a), although its variation  $\log(I)$  versus  $V$  also linear up to about 100 nA. Such multi-barrier behavior has been observed by Gelczuck et al. as well [13], who attributed these two parts of the  $I-V$  curve to an inhomogeneity of the barrier height, a low SBH L-SBH and a higher SBH H-SBH. In that case, the  $\log(I)-V$  characteristics can be divided into two linear regions, giving each an ideality factor and a saturation current extracted from equations (3) and (4).



**Fig. 1.** Forward current–voltage characteristics measured on diodes (a) and (b) of Mo/4H-SiC with a surface area of  $2.48 \text{ mm}^2$  between 80 and 400 K in 20 K increments.

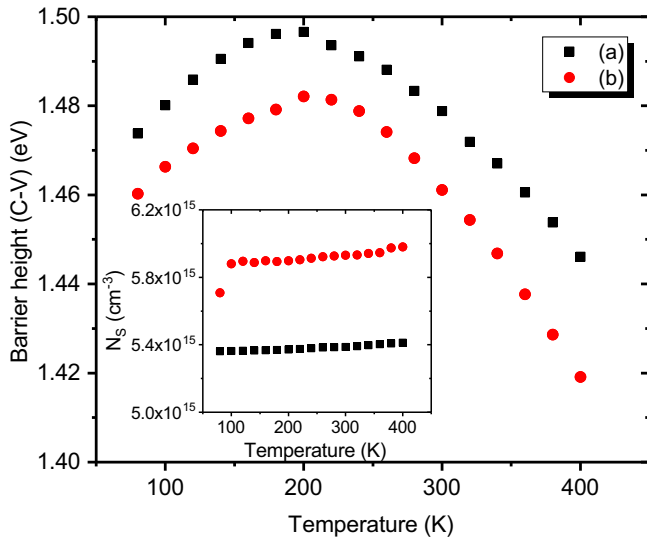
### 4.2 $C-V$ characteristics

Figure 2 shows the barrier height and doping concentration extracted from the  $C-V$  as a function of temperature.  $\Phi_B^{C-V}$  has a maximum around 200 K. Doping slightly increases with temperature, except for diode (b) at 80 K, which may be due to experimental errors.

### 4.3 Flat-band barrier height and modified Richardson plot

The MR plot based on the flat-band barrier height is adopted here since it is possible to extract all the parameters involved with the  $I-V(n, I_s)$  and  $C-V$  curves ( $N_D$ ).

Although  $I-V$  curves appear linear throughout the temperature range for diode (a), the MR plot (Fig. 3a) shows two distinct regions: between 80 and 200 K,  $\Phi_{BF} = 1.20 \text{ eV}$  and between 220 and 400 K,  $\Phi_{BF} = 1.50 \text{ eV}$ .



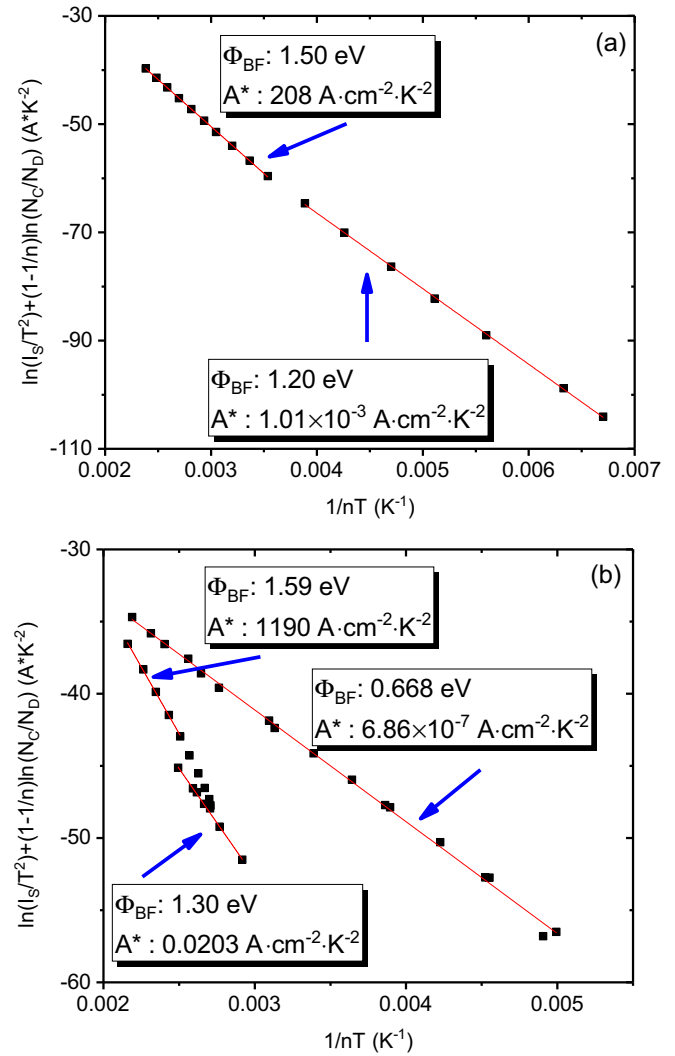
**Fig. 2.** SBH  $\Phi_B^{C-V}$  calculated based on the  $C-V$  characterization. The insert shows the extracted doping concentration.

The diode (b) (Fig. 3b) performs similar barrier heights (1.30 eV between 80 and 160 K, and 1.59 eV between 300 and 400 K). However, it also presents a lower barrier height  $\Phi_{BF} = 0.67$  eV on the full extent of temperature (80–400 K). This lower barrier height is responsible for the excess current at low temperature on (b).  $A^*$  has also been calculated in each linear area of the MR plot using the area  $A$  of the diode, and the value obtained is used for subsequent calculations. Another solution would be to use a constant value  $A^*$  (the higher value extracted, 1.50 eV), and to calculate a ratio between the areas of the high and low SBH regions. The values of  $A^*$  are substantially different from several orders of magnitude in different regions, therefore the surface ratio is rather small. Even for a ratio of  $5.7 \times 10^{-10}$  (area of the low SBH on the total surface of the diode), its influence on the  $I-V$  characteristics cannot be neglected.

For the transition zone between 180 and 280 K on the diode (b), which can result from the coexistence of two different barriers, the value  $A^*$  adopted is that obtained at high temperature.

#### 4.4 Gaussian distribution of barrier height

The curves obtained based on the Gaussian model are linear at each region (Fig. 4). The average value of the barrier height with Gaussian distribution therefore appears as the limit at very high temperature. Consider the common SBH of both diode, for high temperature measurements,  $\overline{\Phi}_{B0} \sim 1.80$  eV for both while at low temperature  $\overline{\Phi}_{B0} \sim 1.17$  eV for (a) and  $\sim 0.73$  eV for (b). The similar H-SBH that dominates at high temperature is close to the barrier that is obtained by  $C-V$ . Indeed, the barrier height extracted by  $C-V$  is always that corresponds to the region of greater contact area [4]. At low temperatures, the two diodes have distinct behavior and the barrier heights are different, but their temperature variations (i.e. standard deviation  $\sigma_s$ ) in each region are



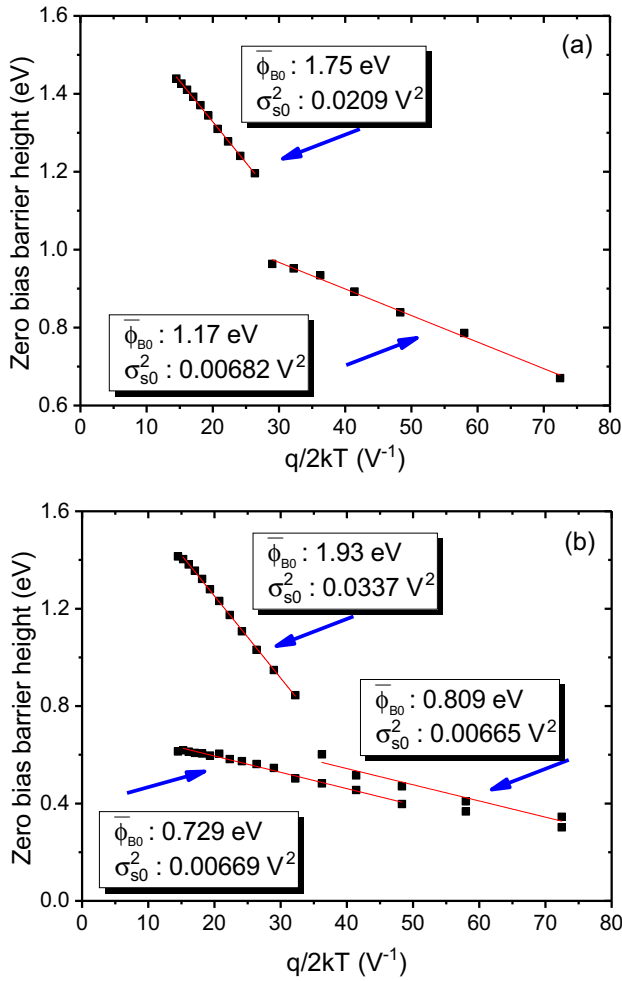
**Fig. 3.** Modified Richardson plot based on flat-band barrier height on diodes (a) and (b).  $n$  and  $I_s$  were calculated in the different linear zones of the  $I-V$  curves, and  $N_D$  is extracted from the  $C-V$  characteristics.

still similar. A more remarkable difference for the transition zone between low and high SBH could be due to the value  $A^*$  adopted in the high temperature zone.

The disadvantage of this model is that it requires to characterize the distribution of barrier height, and to know the value of  $A^*$ , otherwise either using a value from the literature such as Reddy and Reddy [10] or extracting the value from an MR plot (Sect. 4.3).

#### 4.5 Potential fluctuations model

Figure 5 shows the experimental plot of  $1/n - 1$  versus  $q/2kT$ . The values of  $\rho_2$  and  $\rho_3$  are determined from the slope and the intercept. Since the ideality factor  $n$  can be obtained directly from the  $I-V$  curves, thus it can rule out the error from the value of  $A^*$ , unlike in the Gaussian model.



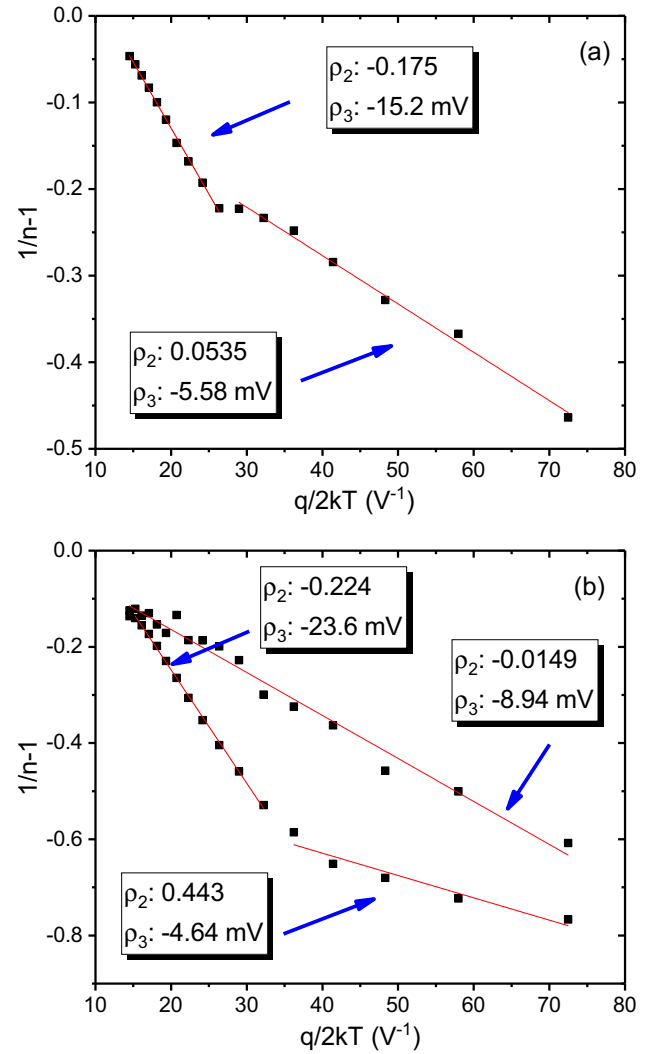
**Fig. 4.** Barrier heights at 0V as a function of temperature for extraction of parameters in Gaussian distribution on diodes (a) and (b).

In the same way as for the Gaussian model, linear zones can be identified and the coefficients  $\rho_2$  and  $\rho_3$  are determined in each zone. Referring to the L-SBH, the coefficients  $\rho_3$  are similar for both diodes ( $\sim -5 \text{ mV}$ ).

#### 4.6 DLTS measurements

Figure 6 shows the DLTS spectra of the diodes after their  $I$ - $V$  and  $C$ - $V$  characterizations between 20 and 550 K. The I-DLTS spectra are dominated by two negative peaks between 70 and 170 K, and two other peaks below 60 K. These defects were analyzed by an Arrhenius plot and their characteristics are gathered in Table 1. The characteristics of other defects that appear on capacitance DLTS spectra are given in Table 2.

As we can see in the Figure 6, the DLTS results are rather similar between the two diodes, with the only exception of the peak around 280 K. Considering the uncertainties inherent in the Arrhenius analysis, the energies extracted for this peak are quite close to each other, which means that the defects observed are common on the two Mo diodes.



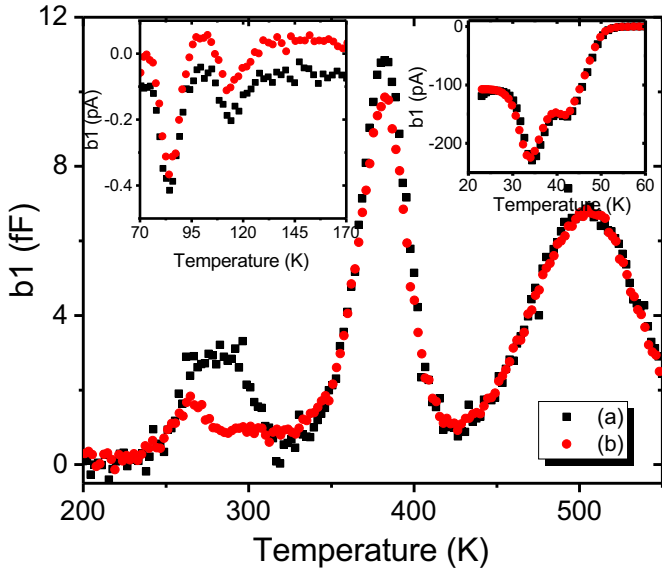
**Fig. 5.** Tracing of  $1/n-1$  in terms of  $q/2kT$  on the diodes (a) and (b).

The physical origin of these defects is specified in Table 3. The two levels at  $\sim 40 \text{ K}$  are attributed to nitrogen. Their activation energies and high concentrations correspond well to the values determined in the literature.

The default at  $0.15 \text{ eV}$  has been attributed to the Ti [24,25] or to the Cr [24–26]. However, apart from unintentional contamination during manufacture, it is difficult to relate this level to Cr or Ti.

The defect  $Z_1/Z_2$  is observed around 280 K, with very low concentrations compared to that measured after radiation (e.g. by protons [18] or Kr ions [19]).

The origins of deeper defects labeled NB and  $RD_{1/2}$  centers are not clear. However, same defect levels detected among Schottky diodes fabricated with various metal contacts on 4H-SiC indicate that their origins are rather related to the SiC than to the metal contact. Similar levels have also been reported by Kawahara et al. after either high temperature oxidation or  $C^+$  implantation followed by Ar annealing and labeled as ON1 and ON2, where these levels are assumed to originate from the interstitials generated at the  $\text{SiO}_2/\text{SiC}$  interface [27].



**Fig. 6.** DLTS signal (correlation  $b1$ ) with a period of 204.8 ms on the diodes (a) and (b) between 200 and 550 K. The inserts are current DLTS (I-DLTS) spectrum between 20 and 60 K (upper right) and 70 to 170 K (top left). The reverse bias  $V_R = -10$  V, the pulse voltage  $V_P = -0.1$  V and the pulse duration  $t_P = 100$  s for all measures.

**Table 1.** Characteristics (activation energy  $E_C - E_T$ , capture cross section  $\sigma$  and concentration  $N_T$ ) obtained by Arrhenius plot of defects detected by I-DLTS (20–170 K). The defects are marked by the peak position of the DLTS signal shown in Figure 6.

$T$ (K)	$E_C - E_T$ (eV)	$\sigma$ (cm <sup>2</sup> )	$N_T$ (cm <sup>-3</sup> )
<b>Diode (a)</b>			
35	0.084	$1.15 \times 10^{-11}$	$8.16 \times 10^{13}$
42	0.087	$1.09 \times 10^{-13}$	$5.26 \times 10^{13}$
83	0.154	$1.56 \times 10^{-15}$	$5.33 \times 10^{12}$
115	0.229	$8.88 \times 10^{-15}$	$1.42 \times 10^{11}$
<b>Diode (b)</b>			
34	0.075	$1.14 \times 10^{-12}$	$8.13 \times 10^{13}$
43	0.083	$4.90 \times 10^{-14}$	$5.24 \times 10^{13}$
84	0.152	$1.19 \times 10^{-15}$	$4.86 \times 10^{12}$
112	0.245	$3.67 \times 10^{-14}$	$3.58 \times 10^{12}$

## 5 Discussion

### 5.1 SBH models

The obtained values of  $\Phi_{B0}$ ,  $\Phi_{BF}$ , and  $\Phi_B^{C-V}$  on both diodes are represented in Figure 7.  $\Phi_{B0}$  varies over the entire temperature range while  $\Phi_{BF}$  almost remains constant, with values close to  $\Phi_B^{C-V}$ . These  $\Phi_{BF}$  values based on equation (11) at each temperature are in good agreement with the values obtained by the MR plot (Fig. 3).

**Table 2.** Characteristics (activation energy  $E_C - E_T$ , capture cross section  $\sigma$  and concentration  $N_T$ ) obtained by Arrhenius plot of defects detected by C-DLTS (200–550 K). The defects are marked by the peak position of the DLTS signal shown in Figure 6.

$T$ (K)	$E_C - E_T$ (eV)	$\sigma$ (cm <sup>2</sup> )	$N_T$ (cm <sup>-3</sup> )
<b>Diode (a)</b>			
280	0.574	$1.49 \times 10^{-15}$	$8.16 \times 10^{11}$
	0.626	$1.84 \times 10^{-15}$	$8.25 \times 10^{11}$
380	0.839	$2.11 \times 10^{-15}$	$3.45 \times 10^{12}$
510	0.863	$1.49 \times 10^{-18}$	$1.01 \times 10^{12}$
	0.914	$8.87 \times 10^{-18}$	$1.81 \times 10^{12}$
<b>Diode (b)</b>			
265	0.517	$2.61 \times 10^{-16}$	$4.73 \times 10^{11}$
295	0.583	$1.12 \times 10^{-16}$	$3.09 \times 10^{11}$
383	0.813	$1.02 \times 10^{-15}$	$2.99 \times 10^{12}$
505	0.946	$8.97 \times 10^{-17}$	$1.25 \times 10^{12}$
	0.944	$1.58 \times 10^{-17}$	$1.69 \times 10^{12}$

**Table 3.** Identification of defects based on the published results, marked by the temperature of the DLTS peak obtained with a window of 204.8 ms. The range of activation energies indicated is extracted from the Tables 1 and 2.

$T$ (K)	$E_C - E_T$ (eV)	Identification	Reference
34–43	0.075–0.087	N	[14–16]
83–84	0.152–0.154	–	–
112–115	0.229–0.245	Metal impurity	[17]
265–295	0.517–0.626	$Z_1/Z_2$	[18–21]
390–393	0.813–0.839	NB center	[22]
505–510	0.863–0.944	$RD_{1/2}$	[18,20,23]

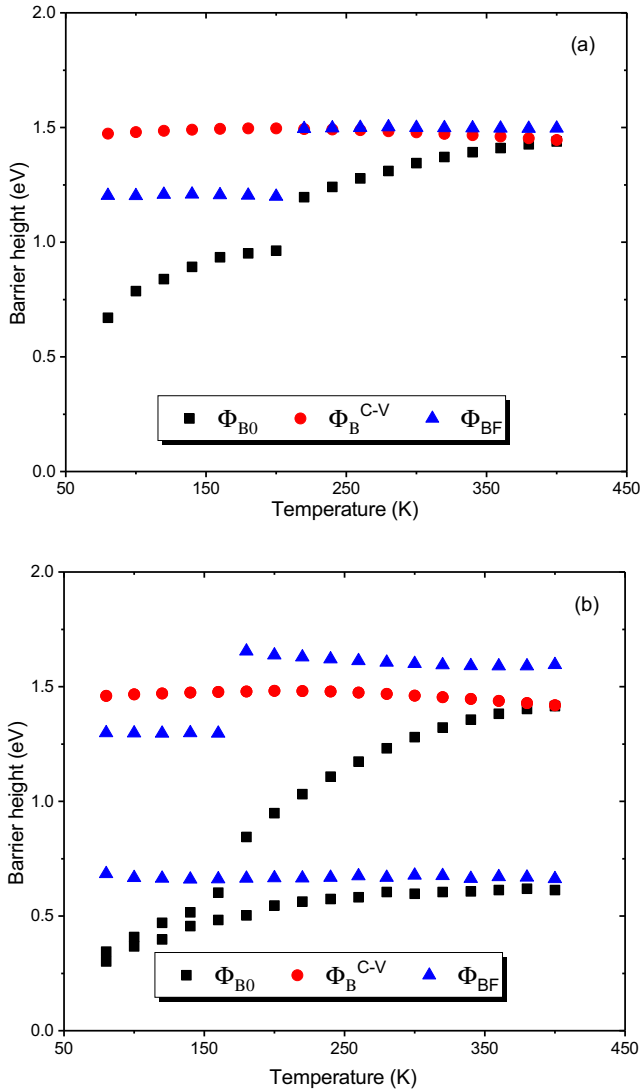
The discontinuity of  $\Phi_{BF}$  results from the change in value of  $A^*$  in both high and low temperature regions. For the diode (b), the difference between  $\Phi_{BF}$  and  $\Phi_B^{C-V}$  is slightly larger due to errors in the MR plot or doping extracted by  $C-V$ .

Consider the  $T_0$  effect where the constant extracted from  $nT$  versus  $T$  plot with a typical value around 100 K in our case. Therefore, equation (11) is reduced to

$$\Phi_{BF} \approx n\Phi_{B0} \quad (15)$$

since the second part of the expression  $\ln(N_C/N_D)kT_0/q$  can be neglected. Equation (15) can well explain the MR plot according to equation (5), where the value extracted should be close to  $\Phi_{BF}$ .

Therefore, by replacing  $\Phi_{B0}$  by  $\Phi_{BF}/n$  in equation (9), the relationship between the ideality factor and the parameters of the Gaussian distribution can be expressed



**Fig. 7.** Temperature evolution of the SBH at 0V  $\Phi_{B0}$  extracted from  $I-V$ , the SBH extracted from  $C-V$   $\Phi_B^{C-V}$  and the flat-band  $\Phi_{BF}$  on the diodes (a) and (b).

in the form:

$$\frac{1}{n} - 1 = \frac{\overline{\Phi}_{B0} - \Phi_{BF}}{\Phi_{BF}} - \frac{q\sigma_{s0}^2}{2kT\Phi_{BF}}. \quad (16)$$

Comparing with equation (10), we can show that:

$$\begin{cases} \rho_2 = \frac{\Phi_{BF} - \overline{\Phi}_{B0}}{\Phi_{BF}} \\ \rho_3 = -\frac{\sigma_{s0}^2}{\Phi_{BF}} \end{cases} \quad (17)$$

or

$$\begin{cases} \overline{\Phi}_{B0} = (1 - \rho_2) \cdot \Phi_{BF} \\ \sigma_{s0}^2 = -\rho_3 \cdot \Phi_{BF} \end{cases} \quad (18)$$

This highlights the links between the Gaussian distribution model and the potential fluctuation model. Mean barrier heights and standard deviations calculated according to equation (18) are given in Table 4, as well as the other parameters used in these two models.

The values between the two models are in good agreement, especially for diode (a). On the diode (b) the slight disagreement could come from either the errors related to the linear approximations of the experimental curves or to the approximation of the equation (15).

## 5.2 Barrier inhomogeneity

The multi-barrier behavior, performs with an excess of forward current at low voltage bias, has often been reported during SBD characterizations, and it was highlighted that these non-ideal diodes occurred regardless of growth technique, pre-deposition cleaning method, or contact metal [28]. Moreover, this double-barrier was found to be more common at lower temperature, which was explained by barrier height inhomogeneities [13].

**Table 4.** Comparison of the parameters of the Gaussian model and the potential fluctuation model. Both parameters  $\overline{\Phi}_{B0}$  and  $\sigma_{s0}^2$  are calculated from equation (18) with  $A^*$  extracted from the MR trace of the Figure 3.

Diode	$\Phi_{BF}$ (eV)	$\rho_2$	$\rho_3$ (mV)	$\overline{\Phi}_{B0}$ (eV)	$\sigma_{s0}^2$ (V <sup>2</sup> )	$\overline{\Phi}'_{B0}$ (eV)	$\sigma_{s0}'^2$ (V <sup>2</sup> )
(a)	1.50	-0.175	-15.23	1.75	0.021	1.76	0.023
	1.20	0.054	-5.58	1.17	0.007	1.14	0.007
	1.59	-0.224	-23.57	1.92	0.034	1.95	0.037
(b)	1.30	0.443	-4.64	0.81	0.007	0.72	0.006
	0.67	-0.015	-8.94	0.73	0.007	0.68	0.006

In order to well explain the multi-barrier behavior of SBD, the first thing that comes to mind is the parallel diode model, which treats the additional low barrier height region as an individual diode that is responsible for the excess current. This model has been numerically discussed by Brezeanu et al. [29] that can explain the relatively low barrier measured at cryogenic temperature. However, this model does not take into consideration of the interaction between low and common SBH regions and cannot explain the size effect: even with the same area ratio of both high and low SBH regions, the effective barrier height increases when the total size is reduced, especially when the dimensions are comparable or shorter than the Debye length of the substrate [30].

A more widely recognized model for barrier inhomogeneity characterization is the one put forward by Tung, where a set of low barrier patches are introduced that would pinch-off under certain condition [31]. Later, this model has been optimized by Gammon et al. by involving the series resistance, thus the  $I$ - $V$ - $T$  data over a wide temperature range can be well reproduced [32].

However, in order to reproduce the  $I$ - $V$  characteristics, a number of parameters must be fixed by fitting in the full temperature range with either Tung's model or its modifications. Therefore, it is not realistic for mass testing, and most of time unnecessary. In fact, with the help of MR plot or even simpler, the potential fluctuation model, it is possible to locate those low SBH regions with large effective area where the double-barrier phenomenon usually results from. These patches may attract more attentions and could be identified by optical or other characterizations much easily.

Since no obvious difference has been observed by DLTS under tested bias condition, the multi-barrier behavior is suggested to be caused by extended interface defects (e.g. dislocation or stacking fault). Therefore, interface defects characterization is supposed for further investigations.

## 6 Conclusion

The two molybdenum-based Schottky diodes have been characterized by  $I$ - $V$ ,  $C$ - $V$ , and DLTS. The MR plot has been adopted to extract barrier height in flat band condition. This circumvents the weak point of the Gaussian distribution model (which requires knowing the value of  $A^*$ ) and improves accuracy over the conventional Richardson plot.

Based on the MR plot, Richardson constants  $A^*$  have been determined in each operating region. The SBH models, namely Gaussian distribution and potential fluctuation, have been verified and main parameters have been calculated from the determined  $A^*$  values. Both models are in good agreement.

Conventional DLTS and I-DLTS analysis have been performed between 200 and 550 K and between 20 and 170 K to investigate the presence of deep level defects. Four defects have been detected and identified at low

temperatures and five at higher temperatures. The  $Z_1/Z_2$  defect is present in small concentration. The origin of high temperature defects is not determined, although they are also listed in the literature.

In any case, no particular difference has been revealed between the two DLTS diodes, which means that the multiple barrier phenomenon on the  $I$ - $V$  curves does not originate in the deep level defects, at least in this temperature range studied.

In addition, the approximation of  $\Phi_{BF}$  as a function of  $\Phi_{B0}$  and  $n$  has been underlined. The result is a link between the Gaussian distribution models and the potential fluctuation model that had not yet been published. The parameters of these two models have been compared using the value of the barrier height in flat band condition, and it appears that the two models are in good agreement.

## Authors contribution statement

Teng Zhang performed the IV and DLTS measurements and analyses the results. Christophe Raynaud helped to analyse, interpret and discuss the results. All authors contributed to improving the paper from version to version. Dominique Planson carefully proofread and assisted in editing the manuscript.

The authors thank the Chinese government for its financial support through a scholarship from the Chinese Scholarship Council (CSC) no. 201506090154, as well as the National Center of Microelectronics of Barcelona and Caly Technologies for the manufacture/design of the components.

## References

1. D. Defives, O. Noblanc, C. Dua, C. Brylinski, M. Barthula, V. Aubry-Fortuna, F. Meyer, IEEE Trans. Electron Devices **46**, 449 (1999)
2. C. Raynaud, K. Isoird, M. Lazar, C.M. Johnson, N. Wright, J. Appl. Phys. **91**, 9841 (2002)
3. S. Bandyopadhyay, A. Bhattacharyya, S.K. Sen, J. Appl. Phys. **85**, 3671 (1999)
4. Y.P. Song, R.L.V. Meirhaeghe, W.H. Lafère, F. Cardon, Solid-State Electron. **29**, 633 (1986)
5. J.H. Werner, H.H. Gttler, J. Appl. Phys. **69**, 1522 (1991)
6. L.F. Wagner, R.W. Young, A. Sugerma, IEEE Electron Device Lett. **4**, 320 (1983)
7. S. Chand, J. Kumar, Semicond. Sci. Technol. **11**, 1203 (1996)
8. R. Hackam, P. Harrop, IEEE Trans. Electron Devices **19**, 1231 (1972)
9. F.A. Padovani, G.G. Sumner, J. Appl. Phys. **36**, 3744 (1965)
10. N.N.K. Reddy, V.R. Reddy, Bull. Mater. Sci. **35**, 53 (2012)
11. S. Chand, J. Kumar, Semicond. Sci. Technol. **10**, 1680 (1995)
12. E.H. Rhoderick, R.H. Williams, *Metal-Semiconductor Contacts*, 2nd edn. (Clarendon Press, Oxford, 1988)
13. L. Gelczuk, P. Kamyczek, E. Paczek-Popko, M. Dbrowska-Szata, Solid-State Electron. **99**, 1 (2014)
14. F. Nava, A. Castaldini, A. Cavallini, P. Errani, V. Cindro, IEEE Trans. Nucl. Sci. **53**, 2977 (2006)



15. E. Omotoso, W.E. Meyer, S.M.M. Coelho, M. Diale, P.N.M. Ngoepe, F.D. Auret, *Mater. Sci. Semicond. Process.* **51**, 20 (2016)
16. T. Kimoto, A. Itoh, H. Matsunami, S. Sridhara, L.L. Clemen, R.P. Devaty, W.J. Choyke, T. Dalibor, C. Peppermüller, G. Pensl, *Appl. Phys. Lett.* **67**, 2833 (1995)
17. A. Uddin, H. Mitsuhashi, T. Uemoto, *Jpn. J. Appl. Phys.* **33**, L908 (1994)
18. D. Davydov, A. Lebedev, V. Kozlovski, N. Savkina, A. Strel'chuk, *Physica B* **308-310**, 641 (2001)
19. E. Kalinina, G. Onushkin, D. Davidov, A. Hallen, A. Konstantinov, V.A. Skuratov, J. Stano, Electrical study of 4H-SiC irradiated with swift heavy ions, in *12th International Conference on Semiconducting and Insulating Materials, 2002, SIMC-XII-2002* (IEEE, 2002)
20. T. Dalibor, G. Pensl, T. Kimoto, H. Matsunami, S. Sridhara, R.P. Devaty, W.J. Choyke, *Diam. Relat. Mater.* **6**, 1333 (1997)
21. Z.Q. Fang, D.C. Look, A. Saxler, W.C. Mitchel, *Physica B* **308-310**, 706 (2001)
22. M. Kato, S. Tanaka, M. Ichimura, E. Arai, *J. Appl. Phys.* **100**, 053708 (2006)
23. K. Danno, T. Kimoto, H. Matsunami, *Appl. Phys. Lett.* **86**, 122104 (2005)
24. N. Ahtziger, W. Witthuhn, *Appl. Phys. Lett.* **71**, 110 (1997)
25. N. Ahtziger, D. Forkel-Wirth, J. Grillenberger, T. Licht, W. Witthuhn, *Nucl. Instrum. Methods Phys. Res. Sect. B* **136-138**, 756 (1998)
26. N. Ahtziger, W. Witthuhn, *Mater. Sci. Eng. B* **46**, 333 (1997)
27. K. Kawahara, J. Suda, T. Kimoto, *J. Appl. Phys.* **111**, 053710 (2012)
28. D.J. Ewing, Q. Wahab, R.R. Ciecchonski, M. Syvjrvi, R. Yakimova, L.M. Porter, *Semicond. Sci. Technol.* **22**, 1287 (2007)
29. G. Brezeanu, G. Pristavu, F. Draghici, M. Badila, R. Pascu, *J. Appl. Phys.* **122**, 084501 (2017)
30. J.L. Freeouf, T.N. Jackson, S.E. Laux, J.M. Woodall, *Appl. Phys. Lett.* **40**, 634 (1982)
31. R.T. Tung, *Phys. Rev. B* **45**, 13509 (1992)
32. P.M. Gammon et al., *J. Appl. Phys.* **114**, 223704 (2013)

**Cite this article as:** Teng Zhang, Christophe Raynaud, Dominique Planson, Measure and analysis of 4H-SiC Schottky barrier height with Mo contacts, *Eur. Phys. J. Appl. Phys.* **85**, 10102 (2019)